FACULTY PROFILE

T.S.R.Krishna Prasad

Department: Electronics and Communication Engineering
College: Gudlavalleru Engineering College
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Address for Communication

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Academic Qualifications

S. No.	Name of the Degree	University	Percentage of Marks	Specialization	Year of Pass
1	(Ph.D.)	Acharya Nagarjuna University	-	Steganalysis (Signal Processing)	Pursuing
2	M.Tech	J.N.T.U. Hyderabad	76	Digital Electronics and Communication Systems	2008
3	B.Tech	Andhra University	68	Electronics and Communication Engineering	2002
4	Diploma	S.B.T.E.T. Hyderabad	85	Electronics and Communication Engineering	1998
5	S.S.C	S.S.C.Board	73	---	1995

Professional Experience

S. No.	Designation	Institution Name	Working Period	
			From	**To**
1	Associate Professor	Gudlavalleru Engineering college, Gudlavalleru	Aug'2008	Till date
2	Assistant Professor	Gudlavalleru Engineering college, Gudlavalleru	June,2004	July,2008
3	Assistant Professor	Dr. Paul Raj Engineering college,Bhadrachalam.	Aug'2002	May, 2004

Papers Published in Journals No. : 16

1. "Enhancing Audio Quality - Noise Cancellation with the LMS Algorithm", Indian Journal of Technical Education,Vol. 12, Issue 1, ISSN: 0971-3034, April 2024.

2. "Real Time Soil Monitoring Using Arduino", International Journal of All Research Education and Scientific Methods (IJARESM),,Vol. 12, Issue 4, ISSN: 2455-6211, April 2024, pp.2122-2127.

3. "Drone Movement Detection Network using Raspberry Pi", International Journal of Innovative Science and Research Technology,Vol. 11, Issue 3, ISSN: 2456-2165,March 2023, pp.1765-1771.

4. "An Effect of Compressive Sensing on Image Steganalysis", International Research Journal of Engineering and Technology,Vol. 10, Issue 3, e-ISSN: 2395-0056,March 2023, pp.1111-1115.

5. "Image Steganography Using Compressive Sensing", International Journal of analytical and experimental modal analysis,Vol. XIV, Issue IV, ISSN: 0886-9367,April 2022, pp.1423-1435.

6. "Universal steganalysis towards higher detection accuracy", International Journal for Electronics Engineering,Vol. 10, Issue 02, ISSN: 0973-7383 ,June2018- December 2018, pp.258-263.

7. "Universal Image Steganalysis using Feature Extraction through the Combinational Transforms", International Journal for Modern Trends in Science and Technology, Vol. 03, Issue 07, ISSN: 2455-3778 ,July2017, pp.306-309.

8. "Hiding of Data in Images using Spread Spectrum Technique", IJCSITS, ISSN: 2249 – 9555, Page No.11-14, Volume 6, Issue 4, and August 2016.

9. "A Robust Image Steganography Embed and Extraction by Using Combinational Transform Technique in painting", IJARCSSE, ISSN: 2277 – 128X, Page No.329-333, Volume 5, Issue 12, and December 2015.

10. "Improved seamless cloning image in painting", IOSR-JVSP, e-ISSN: 2319 – 4197, Page No.38-45, Volume 4, Issue 5, and September 2014.

11. "Implementation of Digital Communication Laboratory on FPGA", IJARCCE, ISSN: 2278 – 1021, Page No.4318-4322, Volume 2, Issue 11, and November 2013.

12. "Raspberry Pi for Commercial Applications", IJCT, ISSN: 2277 – 3061 Volume 11, Issue 2, Page No.2250-2255, and October 2013.

13. "Design and Implementation of Complex Floating point processor using FPGA", VLSICS, Vol.4, No.5, Page No.53-61, October 2013.

14. "An Efficient Reversible Design of BCD Adder", IJCTA, ISSN: 2229-6093, Vol.No.3, ISSUE:5, Page No.1699-1703,2012.

15. "Color Image Contrast Enhancement Using L*a*b* Color space", IJEECS, ISBN: 0974-2166, Vol.No.9, ISSUE: 2, 2012.

16. "Disclosure of Hidden Messages Using Classifier Based on Stastical Moments of Wavelet Characteristic Function", IJECE, ISBN: 0974-2166, Vol.No.4, Page No.243-250, July-December 2011.

Papers Published in Conferences No. :4

1. "Efficient FPGA Implementation of AES Algorithm Well Suited for Embedded Applications", International Conference ICECE, Page No.289-292, 1st-2nd October 2011.

2. "Steganography-Security Through Images", Second International Conference RSPS, Page No.452-455,7-9th January 2010.

3. "Reconstruction of Signal with UnderSampling-Compressed Sampling", Second International Conference RSPS, Page No.532-535,7-9th January 2010.

4. "Design of Generalized Half Band Filters using Elliptic Approximation", National Conference FACE, Page No.30-33,19-21st March 2009.

Workshops /Conferences /Seminars Attended No. : 10

1. Two day seminar on "Advancements in mobile communications" organized by Dept. of ECE,University college of engineering & technology, ANU, Guntur, during 24th-25th February, 2016.

2. One day workshop on "Research in Energy Management Wireless Sensor Networks &Smart Transportation Systems ", organized by Institute of Engineers(India) ,Vijayawada local center, on 29th March , 2015.

3. One day seminar on "Research in Signal and Image processing on FPGA's and Embedded Platforms" organized by Institute of Engineers(India) ,Vijayawada local center, on 22nd November , 2014.

4. One day workshop on "Speech processing: Current Challenges and hands-on Experience", organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during on 23rd August 2014.

5. Three day workshop on "Theory&Applications of intelligent signal processing", organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 28th Febraury 2014 to 2nd March 2014.

6. Two week ISTE workshop on "Signals & Systems" conducted by Indian Institute of Technology Kharagpur from 2nd to 12th January2014 (This workshop was held under the national mission on education through ICT (MHRD)).

7. Two day workshop on "Issues in Integrated Circuit Design", organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 13th - 14th December ,2013.

8. Two week ISTE workshop on "Analog Electronics" conducted by Indian Institute of Technology Kharagpur from 4th to 14th June2013 (This workshop was held under the national mission on education through ICT (MHRD)).

9. Two day ISTE workshop on "Aakash for Education" conducted by Indian Institute of Technology,Bombay from 10th to 11th November 2012 (This workshop was held under the national mission on education through ICT (MHRD)).

10. Two day workshop on "Microwave Engineering Concepts and Lab Practice", organized by   Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 9th - 10th June ,2012.

Online Certifications/Training Programs Attended No. :10

1. AICTE sponsored Online Short Term Training Programme on "Blockchain Architecture Design and Use Cases" (Smart Contracts with Ethereum Solidity) Organized by Department of Electronics & Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, during 24th-29th August 2020.

2. A National Level Webinar on" Intellectual Property Rights & Patents - A View"organized by IQAC, Gudlavalleru Engineering College, Gudlavalleru held during 10th-12th August 2020.

3. AICTE sponsored Online Short Term Training Programme on "Blockchain Architecture Design and Use Cases" (Decentralized Network Synchronization) Organized by Department of Electronics & Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, during 10th-15th August 2020.

4. Online National Level FDP on " Cyber Security" Organized by Department of Computer Science &Engineering and IT in association with CyberSpy, Gudlavalleru Engineering College, Gudlavalleru during 22nd-26th July 2020.

5. One week online Certificate Course on " Digital Teaching Techniques" Organized by ICT Academyduring 29th-04th July2020.

6. One week online Knowledge Development program on " Challenges and Advancements in the Design of IoT,Embedded and VLSI Systems: A Researchers View" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 8th-13th June 2020.

7. One day webinar online training program on " Impact of COVID in Engineering Education &Way forward" Organized by Indian Society for Technical Education(ISTE) Chapter, Gudlavalleru Engineering College, Gudlavalleru on 09th June 2020.

8. Two week online Faculty Development program on " Internet Of Things(IoT)" Organized by Andhra Pradesh State Skill Development Corporation (APSSDC) during 18th-30th May 2020. .

9. One day webinar online training program on " Radio Frequency Measurements for Cellular and Wireless Communication Systems" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 25th May 2020.

10. One day webinar online training program on " Speech Signal Processing" Organized by Department of Department of Computer Science and Engineering, Lakireddy Bali Reddy College of Engineering, Mylavaram on 17th May 2020.

Certifications/Training Programs Attended No. :22

1. One week Faculty Development program on "Applications of Computer Vision using Deep Learning" Organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru during 20 th 25th February 2023.

2. Two day Faculty training program on "Full Custom IC Design using CADENCE Software Tools " Organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru during 4 th- 5th August 2022.

3. Three day training program on "Advanced Development Tools Based IoT" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 11 th-13th November 2019.

4. One week short term training program through ICT mode on "Evaluating Students' Performance and Designing Question Papers" Organized by National Institute of Technical Teachers' Training and Research,Kolkata in Gudlavalleru Engineering College, Gudlavalleru during 25 th Febrauary-01st March 2019.

5. One week Faculty Development program on "Machine Learning Applications in Signal Processing" Organized by the E&ICT Academy, National Institute of Technology, Warangal at Gudlavalleru Engineering College, Gudlavalleru during 04th – 09th February, 2019.

6. Two day training program on " Design and Development of Digital Circuits using VLSI Tools" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6 th-7th December 2018.

7. One week Faculty Development program on "Communications and Signal Processing" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 29 th October-03rd November 2018.

8. One week national level Faculty Development program on "Signal and Image Processing using Labview" Organized by Signal Processing Group of Department of Electronics and Communication Engineering in association with NI Systems Pvt. Ltd. during 13 th- 18th November 2017.

9. Two day faculty training program on "Advances in VLSI Design and Embedded Systems" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th-7th November 2017.

10. Two day staff training program on "MATLAB TOOLS FOR ENGINEERS-HANDS ON EXPERIENCE" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 10th-11th January 2017.

11. One week Faculty Development program on "Hands on Experience in Signal Processing applications using NI Multisim" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 7th-12th November 2016.

12. Two day staff training program on " Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applicationsusing NI Multisim" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 4th-5th November 2016.

13. Three day national level Faculty Development Program on "Emdedded Systems", organized by SAMVID,ECE Association, Organized by Department of Electronics and Communication Engineering, Chalapathi Institute of Engineering and Technology, Guntur during 3rd- 5th Octoberber,2016.

14. A one week Faculty Development Program on "Development of Wireless Sensor Networks using Network Simulator (NS2)- Hands on Experience", Organized by Communication Research group in association with Vertex soft,Hyderabad ,Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 1st- 5th December,2015.

15. One week refresher course on "Microprocessors &Microcontrollers", Organized by R&D Center, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 17th- 21st November, 2015.

16. Two day training program on "Digital Image Processing using MATLAB" Organized by Computer Centre, MIT Campus, Anna University, Chennai during 20th-21st June ,2015.

17. One week refresher course on "Electronic Devices and Circuits", organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 23rd- 28th September ,2013.

18. Two day training program on "MATLAB, SIMULINK & Related Tool Boxes for Engineering Education" Organized by Math Works at Gudlavalleru Engineering College 3rd-4th April ,2013.

19. Two day Faculty Development Program on "VLSI and Embedded System Design", Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 7th- 8th July,2012.

20. One week refresher course on "Pulse& Digital Circuits", Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 21st- 26th November 2011.

21. Five day training program on "Mentor Graphics (Front End and Back end) and Xilinx Tools" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 13th- 17th September 2010.

22. Two week staff Development Program on "Current Trends in Biometrics", Organized by Department of Information Technology, Gudlavalleru Engineering College, Gudlavalleru during 2nd- 14th November 2009.

Subjects Handled

Under Graduation

- Signals & Systems
- Digital Signal Processing
- Digital Image Processing
- PTSP
- Embedded and Real time Operating System
- MPMC
- Analog Communication
- Digital Communication
- Data Communication systems
- Wireless Communication and Networks
- Wireless Sensor Networks
- Bio medical Engineering

Post Graduation

- Digital System Design
- VHDL Modeling of Digital Systems
- Network Security and Cryptography
- Advanced Digital Signal Processing
- Advanced Embedded Systems

Labs Handled

Under Graduation

- DSP LAB

- AC LAB
- DC LAB
- EDC LAB
- PDC LAB
- MWOC LAB
- MPMC LAB
- ECAD LAB
- DCD LAB

Post Graduation
- ASP LAB

Other Responsibilities

College Level
1. CIS 5 coordinator
2. EPABX Coordinator

Department Level
1. NBA Coordinator for criteria 3

2. Department website coordinator

Projects Guided

Under Graduation

S. No.	Name of the project	Year
1	DRONE MOVEMENT DETECTION NETWORK USING RASPBERRY PI	22-23
2	IMAGE STEGANALYSIS USING COMPRESSIVE SENSING	22-23
3	IMAGE STEGANOGRAPHY USING COMPRESSIVE SENSING	21-22
4	IMAGE DEHAZING BY THE USE OF MULTILAYER PERCEPTRON	21-22

24	RAILWAY MAILING SYSTEM	07-08
25	THE CONTOURLET TRANSFORM : AN EFFICIENT DIRECTIONAL MULTIRESOLUTION IMAGE REPRESENTATION	07-08
26	SIMULATION OF RAYLEIGH FADING CHANNEL USING PI/4 DQPSK	05-06

Post Graduation

S.No.	Name of the project	Year
1	UNIVERSAL STEGANALYSIS TOWARDS HIGHER DETECTION ACCURACY	2018
2	UNIVERSAL IMAGE STEGANALYSIS USING FEATURE EXTRACTION THROUGH THE COMBINATIONAL TRANSFORMS	2017
3	HIDING OF DATA IN IMAGES USING SPREAD SPECTRUM TECHNIQUE	2016
4	A ROBUST IMAGE STEGANOGRAPHY USING COMBINATIONAL TRANSFORMATION TECHNIQUE	2015
5	IMPROVED SEAMLESS CLONING OF IMAGE INPAINTING	2014
6	IMPLEMENTATION OF DIGITAL COMMUNICATION LABORATORY ON FPGA	2013
7	DESIGN AND IMPLEMENTATION OF COMPLEX FLOATING POINT PROCESSOR USING FPGA	2013
8	RASPBERRY PI FOR COMMERCIAL APPLICATION	2013
9	COLOR IMAGE CONTRAST ENHANCEMENT USING L*A*B COLOR SPACE	2012
10	AN EFFICIENT REVERSIBLE DESIGN OF BCD ADDER	2012
11	STEGANALYSIS OF IMAGES	2011
12	VLSI IMPLEMENTATION OF HIGH RESOLUTION TIME-TO-DIGITAL CONVERTER	2010

R&D and Consultancy

S. No.	Project Title	Source of Funding	Duration	Role	Status
1	Digitalized Smart college Bus tracking System (Consultancy)	S.V. Technologies, Vijayawada	2 Years	Co-Investigator	Completed

Books Published No. : 4

1. Co-author for chapter 3.1 titled "Digital Communication Lab" of Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016
2. Co-author for chapter 4.1 titled "Digital Communication Lab" of Electronics and Communication Engineering laboratory manual series with ISBN – 978-1-4276-5563-9 in the year 2016
3. Co-author for chapter 5 titled "Analog Communication" of Electronics and Communication Engineering Instructional manual with ISBN – 978-1-4276-5539-4 in the year 2015.
4. Co-author for chapter 5.2 titled "Microwave & Optical Communication Lab" of Electronics and Communication Engineering laboratory Manual with ISBN – 978-1-4276-5536-3 in the year 2015.

Curriculum Design and Development

1. PTRV
2. ANALOG COMMUNICATION
3. ASP LAB

Extra Curricular Activities

- Enacted as BoS member for the Autonomous curriculum design of Electronics and Communication Engineering at Gudlavalleru Engineering College, Gudlavalleru on 2nd August 2014 and 25th May 2015.

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: Signature

Place: (T.S.R.KRISHNA PRASAD)